

IR3312(S)

PROGRAMMABLE CURRENT SENSING HIGH SIDE SWITCH

Features

- Load current feedback
- Programmable over current shutdown
- Active clamp
- E.S.D protection
- Input referenced to Vcc
- Over temperature shutdown
- Reverse battery protection

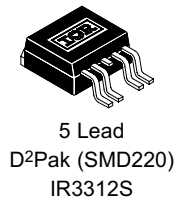
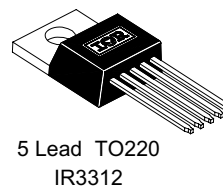
Description

The IR 3312(S) is a Fully Protected 4 terminal high side switch. The input signal is referenced to Vcc. When the input voltage $V_{cc} - V_{in}$ is higher than the specified V_{ih} threshold, the output power MOSFET is turned-on. When $V_{cc} - V_{in}$ is lower than the specified V_{il} threshold, the output MOSFET is turned-off. A sense current proportional to the current in the power Mosfet is sourced to the Ifb pin. Over-current shutdown occurs when $V_{ifb} - V_{in} > 4.5 V$. The current shutdown threshold is adjusted by selecting the proper Rlfb. Either over-current and over-temperature latches off the switch. The device is reset by pulling the input pin high. Other integrated protections (ESD, reverse battery, active clamp) make the IR3312(S) very rugged and suitable for the automotive environment.

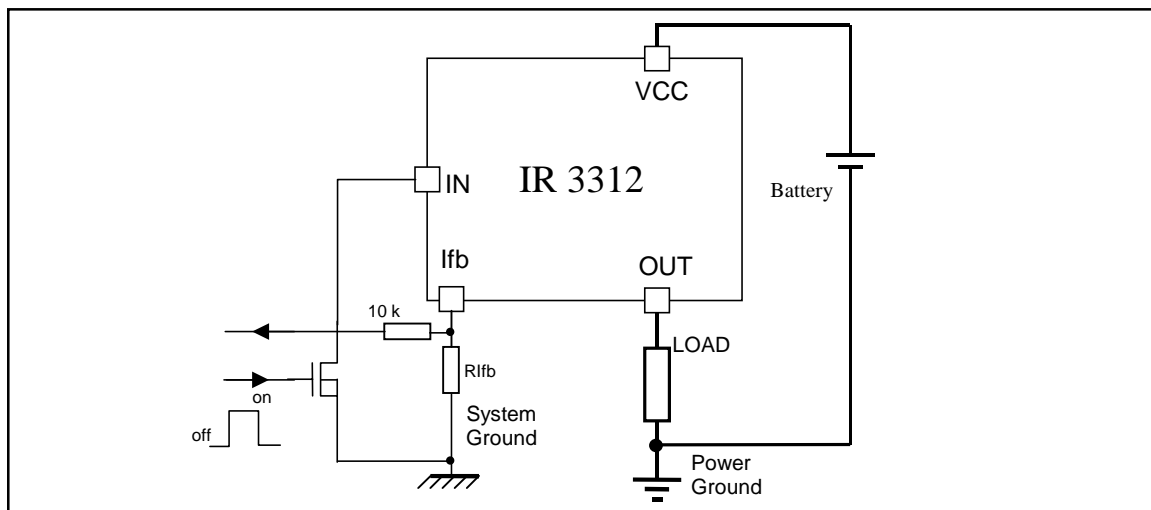
Product Summary

| | |
|----------------|--------------|
| $R_{ds(on)}$ | 20m Ω |
| $V_{cc.op.}$ | 6 to 28V |
| Current ratio | 2800 |
| $I_{shutdown}$ | 3 to 30A |
| Active clamp | 35V |
| Load Dump | 40V |

Package



Typical Connection



Absolute Maximum Ratings

Absolute maximum ratings indicate sustained limits beyond which damage to the device may occur. All voltage parameters are referenced to Vcc lead. (T_{Ambient} = 25°C unless otherwise specified).

| Symbol | Parameter | Min. | Max. | Units |
|---|---|------|------|-------|
| V _{cc} - V _{in} max | Maximum input voltage | -16 | 50 | V |
| V _{cc} -V _{lfb} max | Maximum lfb voltage | -0.3 | 50 | |
| V _{cc} - V _{out} max. | Maximum output voltage | -0.3 | 33 | |
| I _{ds} cont. | Diode max. permanent current (R _{th} = 60 °C/W) (1) | — | 2.8 | A |
| I _{ds1} cont | Diode max. permanent current (R _{th} = 5 °C/W) (1) | — | 25 | |
| I _{ds} pulsed | Diode max. pulsed current (1) | — | 100 | |
| ESD 1 | Electrostatic discharge (human body model)C=100pF, R=1500Ω, | — | 4 | kV |
| ESD 2 | Electrostatic discharge (machine model)C=200pF,R=0Ω, L=10μH | — | 0.5 | |
| P _d | Power dissipation (R _{th} = 60 °C/W) | — | 2 | W |
| T _J max. | Max. storage and junction temperature | -40 | 150 | °C |
| Min R _{lfb} | Minimum resistor on the lfb pin | 0.3 | — | kΩ |
| I _{fb} max | Max. lfb current | -20 | +20 | mA |

Thermal Characteristics

| Symbol | Parameter | Typ. | Max. | Units |
|-------------------|--|------|------|-------|
| R _{th} 1 | Thermal resistance junction to Ambient - TO220 | 60 | — | °C/W |
| R _{th} 2 | Thermal resistance junction to case - TO220 | 0.7 | — | |
| R _{th} 1 | Thermal resistance with standard footprint - SMD220 | 60 | — | |
| R _{th} 2 | Thermal resistance with 1" square footprint - SMD220 | 35 | — | |
| R _{th} 3 | Thermal resistance junction to case - SMD220 | 0.7 | — | |

Recommended Operating Conditions

These values are given for a quick design. For operation outside these conditions, please consult the application notes.

| Symbol | Parameter | Min. | Max. | Units |
|-----------------------------------|---|------|------|-------|
| V _{cc} -V _{in} | Continuous input voltage | 6 | 28 | V |
| V _{cc} -V _{lfb} | Continuous lfb pin voltage | 0.3 | 28 | |
| V _{cc} | Supply to power ground voltage | 6 | 28 | |
| I _{out} | Continuous output current (R _{th} /amb < 5 °C/W, T _j = 125°C) | — | 14 | A |
| I _{out} 85°C amb. | Continuous output current (R _{th} /amb < 60 °C/W, T _j = 125°C) | — | 3.9 | |
| R _{lfb} | lfb resistor to program I _{sd} and scale (2 & 3) | 0.5 | 3.5 | kΩ |
| Pulse min. | Minimum turn-on pulse width | 1 | — | ms |
| F _{max} | Maximum operating frequency | — | 500 | Hz |
| F _{max} Prot. | Maximum frequency with protections activated | — | 200 | |

- 1) Limited by junction temperature. Pulsed current is also limited by wiring
- 2) <500 Ohm or shorting lfb to gnd may damage the part with I_{sd} around 37A
- 3) >5000 Ohm or leaving lfb open will shutdown the part. No current will flow in the load.

Protection Characteristics

T_j = 25°C (unless otherwise specified), R_{lfb} = 500 to 5kΩ.

| Symbol | Parameter | Min. | Typ. | Max. | Units | Test Conditions |
|--|-------------------------------------|------|------|------|-------|---|
| V _{lfb} -V _{in} @ I _{sd} | Over-current shutdown threshold | 4 | 4.5 | 5.4 | V | |
| T _{sd} | Over-temp. shutdown threshold | — | 165 | — | °C | see Fig. 7 |
| T _{reset} | Protection reset time | — | 50 | 300 | μs | see Fig. 7 |
| OV | Over voltage shutdown (not latched) | 33 | 36 | 41 | V | |
| I _{sd} f | Fixed over current shutdown | 30 | 37 | 45 | A | V _{lfb} <V _{in} |
| I _{sd} _1k | Adjustable over current shutdown 1K | 9 | 12 | 16 | A | R _{lfb} =1k |
| Min.Pulse | Minimum pulse width (no WAIT state) | 200 | 500 | 1200 | μs | see Fig. 6 |
| WAIT | WAIT function timer | 0.5 | 1.2 | 3.5 | ms | see Figs. 6 and 7 |
| Rev.Rds _{on} | Rds(on) reverse battery protection | 10 | 16 | 28 | mΩ | V _{cc} -V _{in} =-12V, I _{load} =5A |

Static Electrical Characteristics

(T_j = 25°C, V_{cc} = 14V unless otherwise specified.)

| Symbol | Parameter | Min. | Typ. | Max. | Units | Test Conditions |
|---------------------|--|------|------|------|-------|---|
| I _q | Total quiescent current (I _{out} +I _{fb}) | — | 22 | 50 | μA | V _{cc} -V _{in} =0, V _{cc} -V _{out} =12V V _{cc} -V _{lfb} =12V |
| I _{in} | Input current | 1.5 | 4 | 6 | mA | V _{cc} -V _{in} =14V |
| V _{ih} | High level input threshold voltage (4) | — | 5 | 5.5 | | |
| V _{il} | Low level input threshold voltage (4) | 3.5 | 4 | — | V | |
| V _{hys} | Input hysteresis = V _{ih} -V _{il} | 0.4 | 1 | 1.5 | | |
| I _{out} qs | Output quiescent current | — | 9 | 15 | μA | V _{cc} -V _{in} =0; V _{cc} -V _{lfb} =0; V _{cc} -V _{out} =12V |
| R _{ds1} on | ON state resistance (5) | 10 | 15 | 20 | | I _{out} =15A, V _{cc} -V _{in} =14V |
| R _{ds2} on | ON state resistance (5) | 10 | 16 | 28 | mΩ | I _{out} =10A, V _{cc} -V _{in} =6V |
| R _{ds3} on | ON state resistance (5) | 20 | 28.5 | 38 | | I _{out} =15A, T _j = 150°C |
| V _{clamp1} | V _{cc} to V _{out} active clamp voltage | 33 | 35 | — | | I _{out} = 10mA |
| V _{clamp2} | V _{cc} to V _{out} active clamp voltage | — | 36 | 40 | | I _{out} =30A, V _{cc} -V _{lfb} <20V |
| V _{sd} | Body diode forward voltage | — | 0.85 | 1 | V | I _{out} =5A |
| V _{aval.} | V _{cc} to V _{out} avalanche voltage | 40 | 43 | 50 | | I _{out} =100mA, V _{cc} -V _{lfb} > 35V |

Switching Electrical Characteristics

V_{cc} = 14V, Resistive Load = 0.4Ω, T_j = 25°C, (unless otherwise specified).

| Symbol | Parameter | Min. | Typ. | Max. | Units | Test Conditions |
|-------------------|---|------|------|------|-------|-----------------|
| T _{don} | Turn-on delay time to V _{cc} -V _{out} = 0.9 V _{cc} | 2 | 9 | 20 | | see figure 2 |
| T _{r1} | Rise time to V _{cc} -V _{out} =5V | 0.2 | 1 | 5 | μs | |
| T _{r2} | Rise time to V _{cc} -V _{out} = 0.1V _{cc} | 5 | 15 | 60 | μs | |
| E _{on} | Turn ON energy | — | 0.75 | 1.5 | mJ | see figure 3 |
| T _{doff} | Turn-off delay to V _{cc} -V _{out} = 0.1V _{cc} | 5 | 14 | 60 | μs | |
| T _f | Fall time to V _{cc} -V _{out} = 0.9 V _{cc} | 2 | 7 | 25 | μs | |
| E _{off} | Turn OFF energy | — | 0.5 | 1 | mJ | |

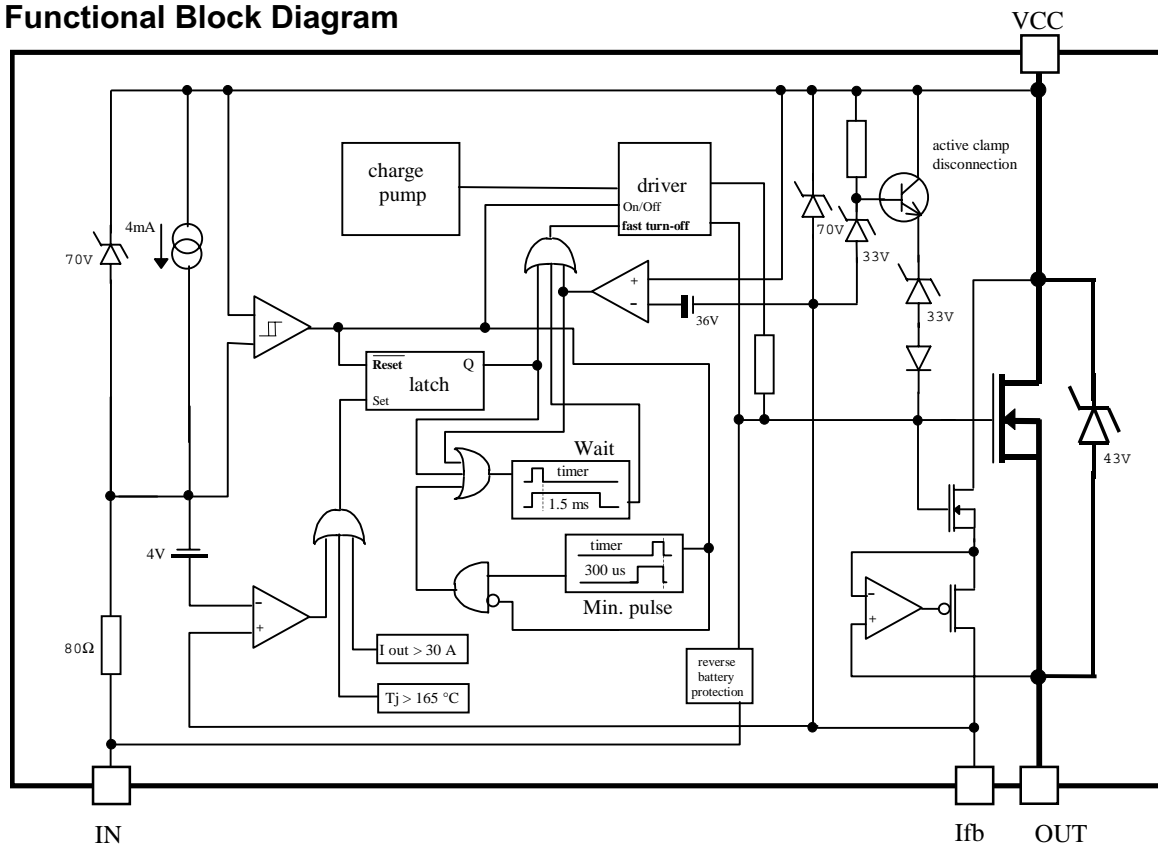
4) Input thresholds are measured directly between the input pin and the tab. Any parasitic resistance in common between the load current path and the input signal path can significantly affect the thresholds.

5) Rds(on) is measured between the Tab and the Out pin, 5mm away from the package.

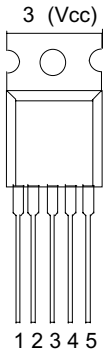
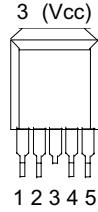
Current Sense Characteristics

| Symbol | Parameter | Min. | Typ. | Max. | Units | Test Conditions |
|-----------------|---|-------|-------|-------|-------|--|
| Ratio | I load / I _{fb} current ratio | 2,400 | 2,800 | 3,300 | — | T _j = 25°C, R _{fb} = 500Ω, I = 30A |
| Ratio_TC offset | I _{load} /I _{fb} variation over temperature | -5 | 0 | +5 | % | T _j = -40 T ₀ +150°C |
| Trst | I _{fb} response time (low signal) | — | 5 | 15 | μs | 90% of the I load step |

Functional Block Diagram



Lead Assignments

| | |
|--|--|
|  <p>3 (Vcc) 1 2 3 4 5</p> <p>5 Lead - TO220</p> |  <p>3 (Vcc) 1 2 3 4 5</p> <p>5 Lead - D²PAK (SMD220)</p> |
| <p>1 - In 2 - Ifb 3 - Vcc (tab) 4 - NC 5 - Out</p> | |
| IR3312 | IR3312S |
| Part Number | |

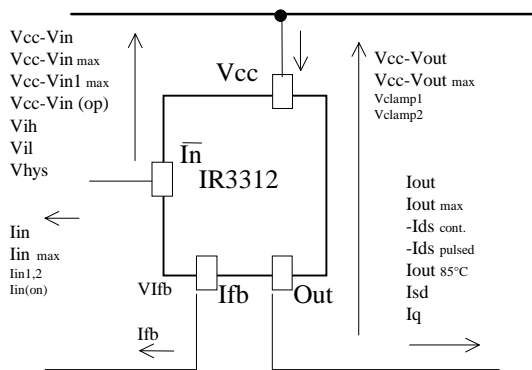


Figure 1 - Voltages and currents definitions

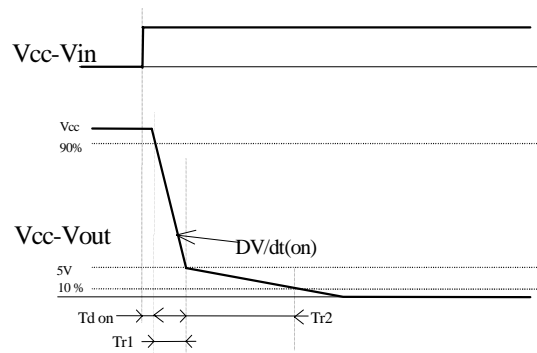


Figure 2 - Switching time definitions (turn-on)

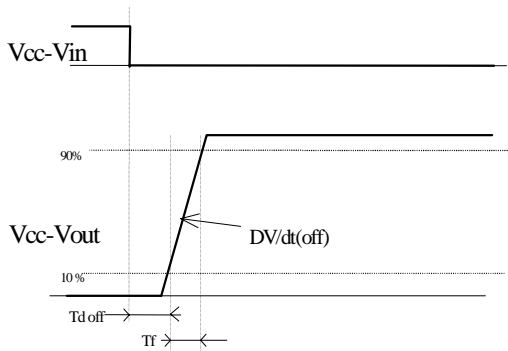


Figure 3 - Switching time definitions (turn-off)

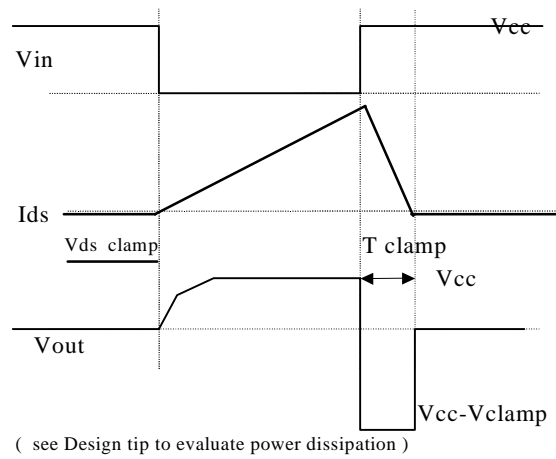
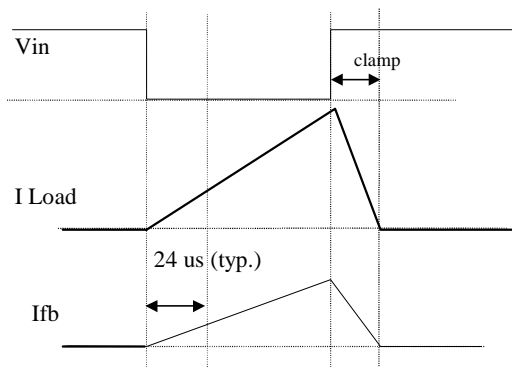


Figure 4 - Active clamp waveforms



Precise measurement

Figure 5 - Current sense precision:
Accurate measurement only when the power Mosfet is fully ON

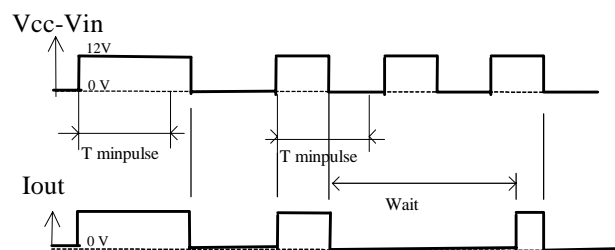


Figure 6 - Minimum pulse & WAIT function

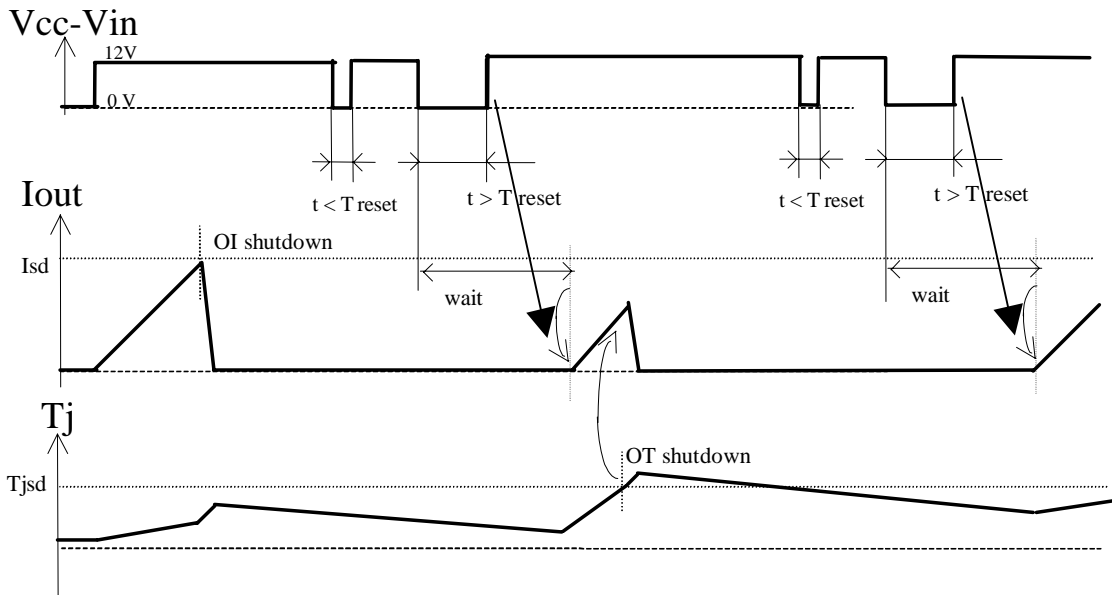


Figure 7 - Protection Timing Diagrams

All curves are typical characteristics. Operation in hatched areas is not recommended. $T_j=25^{\circ}C$, $R_{lfb}=500\ \Omega$, $V_{cc}=14V$ (unless otherwise specified).

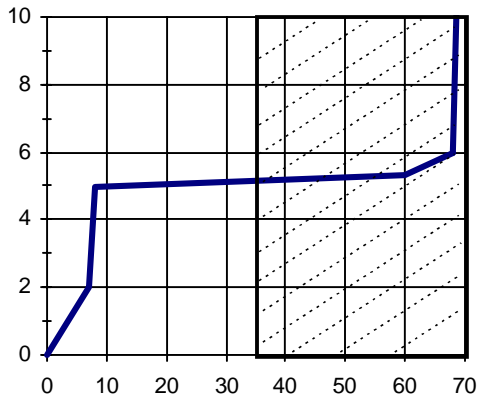


Figure 8 - I_{cc} (mA) vs V_{cc-Vin} (V)

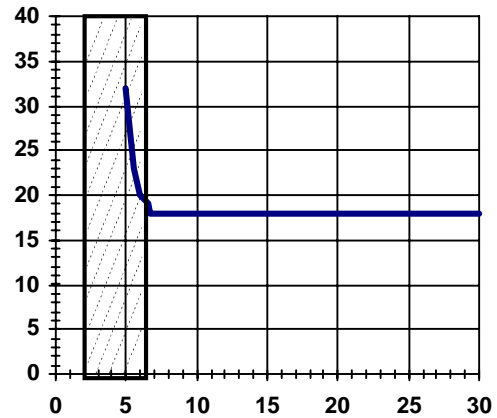


Figure 9- R_{dsOn} (m Ω) vs V_{cc-Vin} (V)

IR3312(S)

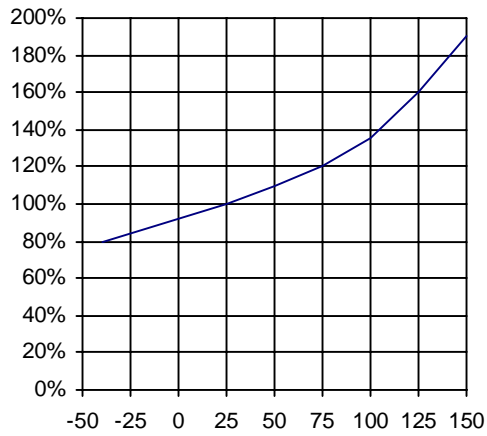


Figure 10 - Normalized Rdson (%) vs Tj (°C)

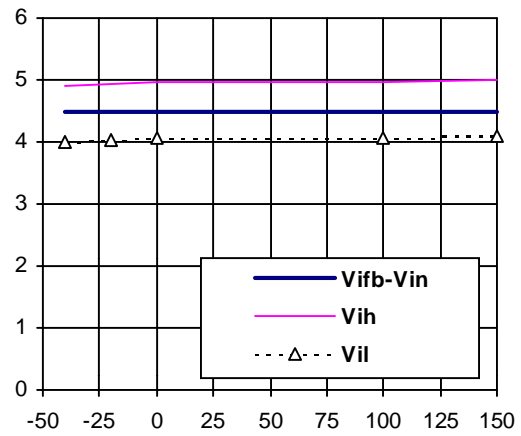


Figure 11 - Vih, Vil & Vifb - Vin (V) vs Tj (°C)

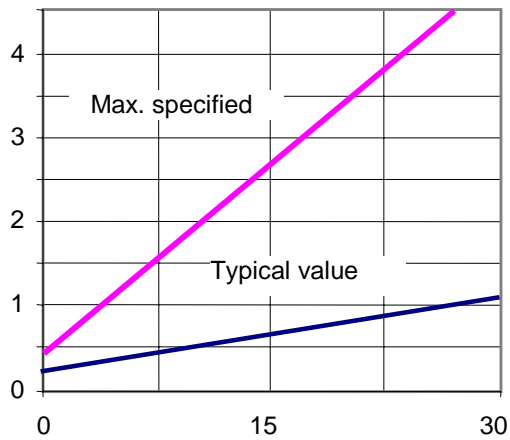


Figure 12 - Error (+/-A) vs Iload (A)

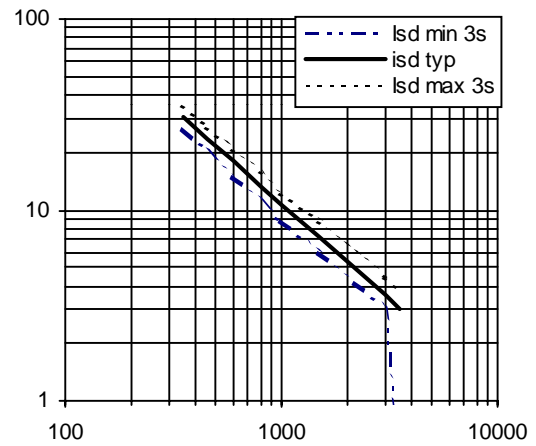


Figure 13 - Isd (A) vs Rlfb (Ohm)

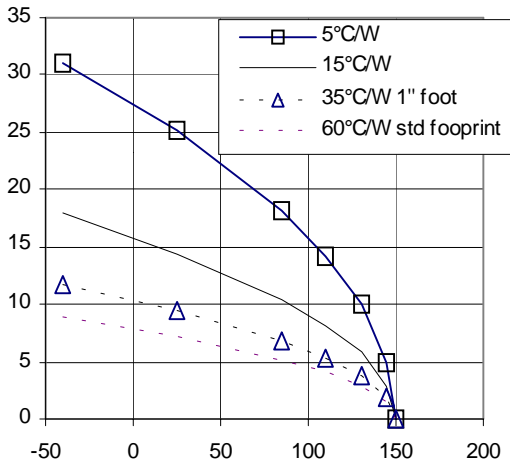


Figure 14 - Max. DC current (A) vs Temp. (°C)

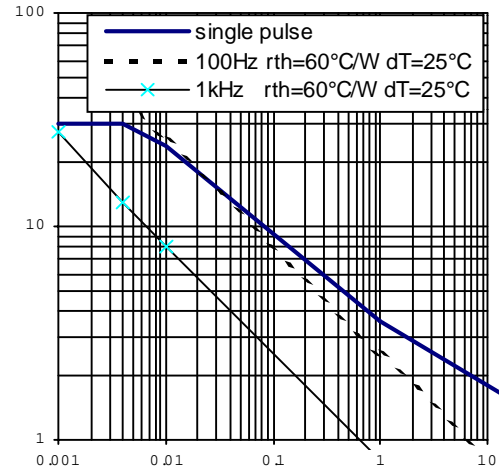


Figure 15 - Max. I (A) vs inductance (mH)

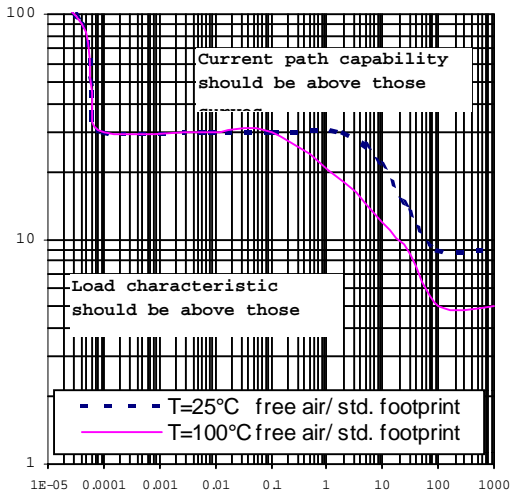


Figure 16 - I out (A) vs Protection resp. Time (s)

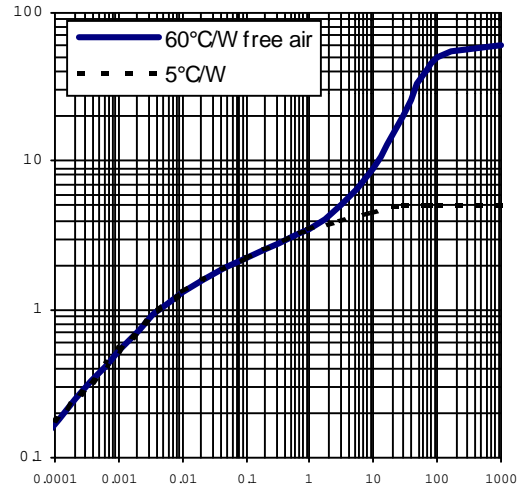
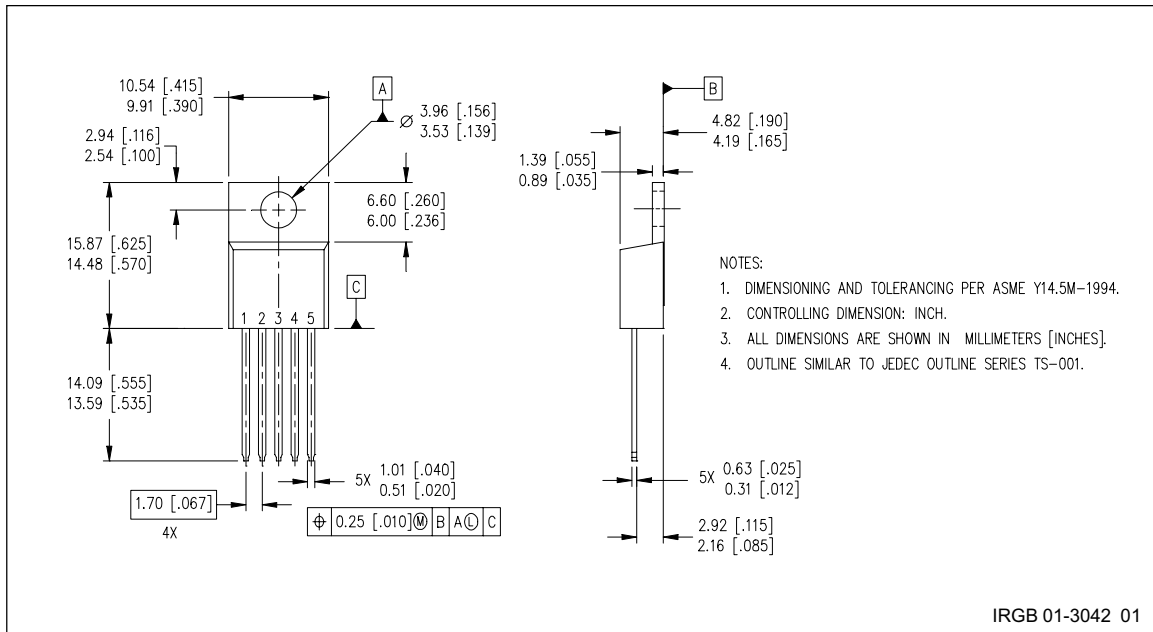
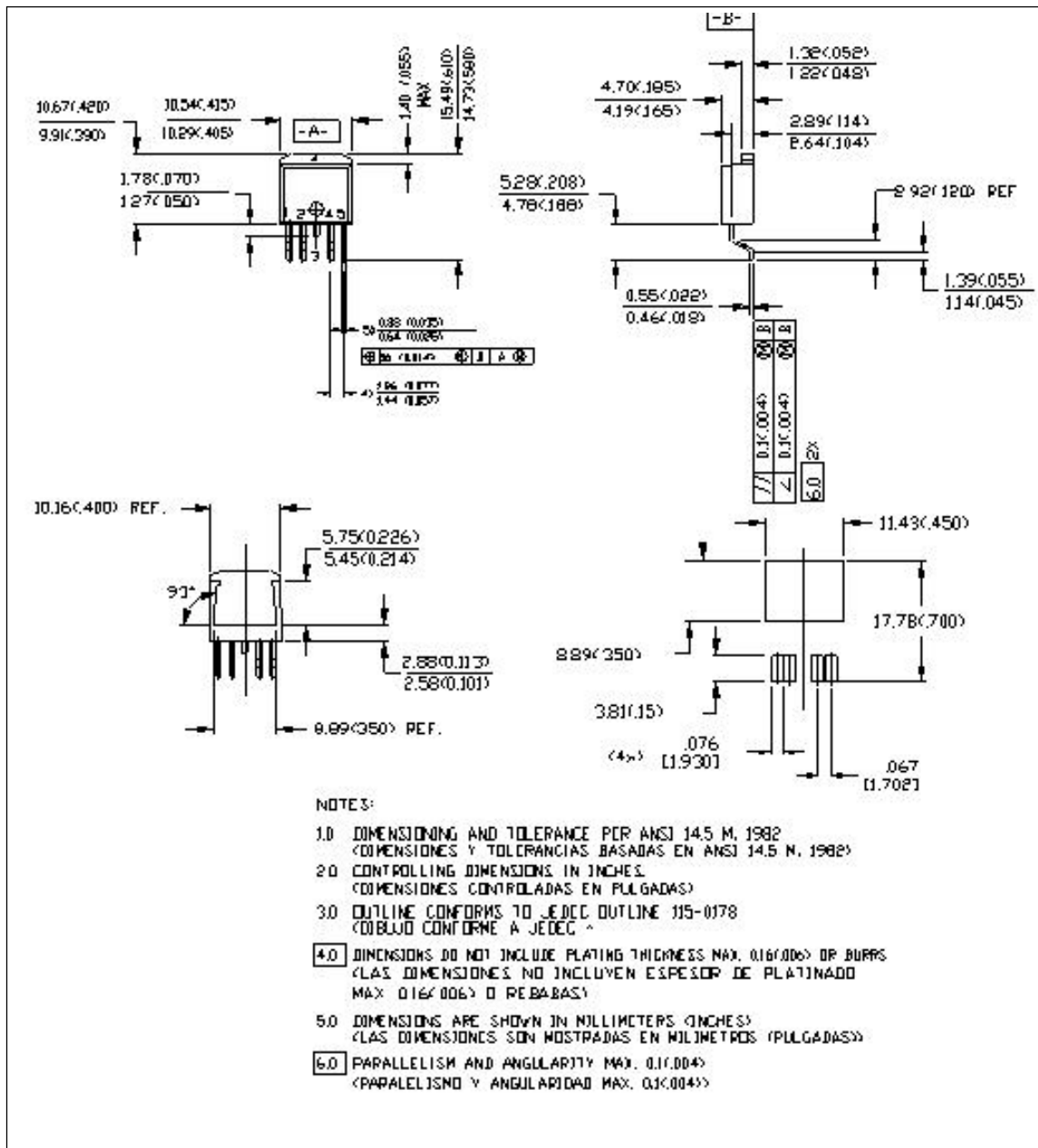


Figure 17 - Rth (°C/W) vs Time (s)

Case Outline - TO220 (5 lead)



Case Outline 5 Lead - D²PAK (SMD220)



Note: For the most current drawings please refer to the IR website at:
<http://www.irf.com/package/>